



\*\*FILE\*\* ID\*\*RM1DISCON

G 10

RRRRRRRRR MM MM 11 DDDDDDDDD IIIIII SSSSSSSSS CCCCCCCCCC 000000 NN NN  
RRRRRRRRR MM MM 11 DDDDDDDDD IIIIII SSSSSSSSS CCCCCCCCCC 000000 NN NN  
RR RR MMMMM MMMMM 1111 DD DD SS CC 00 00 NN NN  
RR RR MMMMM MMMMM 1111 DD DD SS CC 00 00 NN NN  
RR RR MM MM MM 11 DD DD SS CC 00 00 NNNN NN  
RR RR MM MM MM 11 DD DD SS CC 00 00 NNNN NN  
RRRRRRRRR MM MM 11 DD DD SSSSSSSS CC 00 00 NN NN NN  
RRRRRRRRR MM MM 11 DD DD SSSSSSSS CC 00 00 NN NN NN  
RR RR MM MM 11 DD DD SS CC 00 00 NN NNNN  
RR RR MM MM 11 DD DD SS CC 00 00 NN NNNN  
RR RR MM MM 11 DD DD SS CC 00 00 NN NN  
RR RR MM MM 11 DD DD SS CC 00 00 NN NN  
RR RR MM MM 111111 DDDDDDDDD IIIIII SSSSSSSSS CCCCCCCCCC 000000 NN NN  
RR RR MM MM 111111 DDDDDDDDD IIIIII SSSSSSSSS CCCCCCCCCC 000000 NN NN

The diagram consists of a 10x10 grid of squares. The top-left portion of the grid contains 'L' shaped blocks, while the bottom-right portion contains 'S' shaped blocks. The 'L' blocks are arranged in a pattern where each row has one more 'L' than the previous row, starting from two in the first row up to ten in the tenth row. The 'S' blocks are arranged in a similar pattern, starting with two in the first row and increasing by one each row until ten in the tenth row. The blocks overlap, with some squares containing both an 'L' and an 'S'.

(2) 53  
(3) 75

DECLARATIONS  
RM\$DISCONNECT1 - SEQ. FILE ORG. SPECIFIC DISCONNECT CODE

0000 1 \$BEGIN RM1DISCON,000,RMSRMS1,<DISCONNECT FOR SEQ. ORG.>  
0000 2  
0000 3 \*\*\*\*\*  
0000 4 \*  
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0000 22 \*  
0000 23 \*  
0000 24 \*\*\*\*\*  
0000 25  
0000 26 ++  
0000 27 FACILITY: RMS32  
0000 28  
0000 29 ABSTRACT: Module to give back all storage associated with IRAB.  
0000 30  
0000 31  
0000 32  
0000 33 ENVIRONMENT: STAR processor running STARLET EXEC.  
0000 34  
0000 35  
0000 36 AUTHOR: L F Laverdure. CREATION DATE: 31-Mar-1977  
0000 37  
0000 38 MODIFIED BY:  
0000 39  
0000 40 V03-001 KBT0138 Keith B. Thompson 20-Aug-1982  
0000 41 Reorganize psects  
0000 42  
0000 43 V02-008 REFORMAT Frederick E. Deen, Jr. 25-Jul-1980  
0000 44 This code was reformatted to adhere to RMS standards  
0000 45  
0000 46 V007 PSK001 P S Knibbe 05-Dec-1979  
0000 47 On foreign magtapes the IRBSV\_EOF bit is moved into  
0000 48 the IFBSV\_EOF bit.  
0000 49  
0000 50  
0000 51 --

0000 53 .SBTTL DECLARATIONS  
0000 54  
0000 55  
0000 56 : INCLUDE FILES:  
0000 57 :  
0000 58  
0000 59 :  
0000 60 : MACROS:  
0000 61 :  
0000 62  
0000 63 \$IFBDEF  
0000 64 \$IRBDEF  
0000 65 \$DEVDEF  
0000 66  
0000 67 :  
0000 68 : EQUATED SYMBOLS:  
0000 69 :  
0000 70 :  
0000 71 : OWN STORAGE:  
0000 72 :  
0000 73

0000 75 .SBTTL RM\$DISCONNECT1 - SEQ. FILE ORG. SPECIFIC DISCONNECT CODE  
0000 76  
0000 77 ++  
0000 78 RM\$DISCONNECT1 - Sequential file organization specific DISCONNECT code  
0000 79  
0000 80 1.0 If not doing BLOCK I/O, call RMSWTLST1 to write out last  
0000 81 block with padding and extending as required.  
0000 82 2.0 If disk reset IFBSV\_EOF.  
0000 83 2.5 If foreign magtape, move IRBSV\_EOF bit to IFBSV\_EOF bit  
0000 84 3.0 Jump to RM\$DISCOMMON  
0000 85  
0000 86 CALLING SEQUENCE:  
0000 87  
0000 88 BSBW RM\$DISCONNECT1  
0000 89  
0000 90 (entered at RM\$DISCONNECT1 via case branch from  
0000 91 RM\$DISCONNECT with return PC on STACK)  
0000 92  
0000 93 INPUT PARAMETERS:  
0000 94  
0000 95 R11 IMPURE AREA address  
0000 96 R10 IFAB address  
0000 97 R9 IRAB address  
0000 98 R8 RAB address  
0000 99  
0000 100 IMPLICIT INPUTS:  
0000 101  
0000 102 The contents of the various RMS internal structures  
0000 103  
0000 104 OUTPUT PARAMETERS:  
0000 105  
0000 106 R0 STATUS CODE  
0000 107 R1-R7,AP destroyed  
0000 108  
0000 109 IMPLICIT OUTPUTS:  
0000 110  
0000 111 IFBSV\_EOF cleared  
0000 112 The implicit outputs of RMSWTLST1  
0000 113  
0000 114 COMPLETION CODES:  
0000 115  
0000 116 Standard RMS  
0000 117  
0000 118 SIDE EFFECTS:  
0000 119  
0000 120 May be running at AST level.  
0000 121 ;--  
0000 122

			0000	124	RMSDISCONNECT1::		
			0000	125	\$TSTPT DISCON1		
OC	50	01	DO	0006	MOVL #1, R0	; anticipate success	
	69	22	E0	0009	BBS #IRBSV_PPF_IMAGE,(R9),10\$	; branch if indirect PPF	
			000D	126		; to avoid write	
07	22	AA	05	E0	000D	BBS #IFBSV_BIO,IFBSB_FAC(R10),10\$	; branch if BLOCK I/O
	03	69	27	E0	0012	BBS #IRBSV_BIO_LAST,(R9),10\$	; branch if last operation
			0016	128		was a BLOCK I/O operation	
			0016	129		(mixed block and rec. ops)	
			0016	130		; write last block if needed	
	FFE7'	30	0016	131		; branch if not disk	
	1C	E1	0019	132			
04	6A		001B	133	BSBW RMSWTLST1		
			001D	134	10\$: BBC #DEV\$V RND,-		
10	05	E1	0021	135	IFBSL PRIM_DEV(R10),15\$	; clear EOF flag	
	6A		0023	136	CSB #IFBSV_EOF,(R10)	; branch if not magtape	
	1B	E1	0025	137	BBC #DEV\$V SQD,-		
OC	6A		0027	138	IFBSL PRIM_DEV(R10),20\$		
			0029	139	BBC #DEV\$V FOR,-	; branch if not foreign	
04	69	21	E1	002D	140	IFBSL PRIM_DEV(R10),20\$	
			0031	141	CSB #IFBSV_EOF,(R10)	; assume it's not at EOF	
09	22	AA	FFCB'	30	0035	BBC #IRBSV_EOF,(R9),20\$	; that's right
	05	E1	0038	142	SSB #IFBSV_EOF,(R10)	; nope - set IFAB bit	
			003D	143	BSBW RMSDISCOMMON	; go finish up	
			003D	144	20\$: BBC #IFBSV_BIO,IFBSB_FAC(R10),30\$	; branch if not BLOCK I/O	
			003D	145			
			003D	146			
			003D	147	:		
			003D	148	: This connect was for BLOCK I/O.		
			003D	149	: Reset to BRO if also set.		
			003D	150	:		
04	22	AA	06	E1	003D	003D 151	
			22	AA	0042	BBC #IFBSV_BRO,IFBSB_FAC(R10),30\$	; branch if BRO not also set
			20		0046	BICB2 #IFBSM_BIO,IFBSB_FAC(R10)	; clear BIO
			05		0047	RSB .END	; return to caller
				154	30\$:		
				155			

RM1DISCON  
Symbol table

DISCONNECT FOR SEQ. ORG.

M 10

16-SEP-1984 00:47:00 VAX/VMS Macro V04-00  
5-SEP-1984 16:23:17 [RMS.SRC]RM1DISCON.MAR;1

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\$\$.PSECT EP  
\$\$RMSTEST  
\$\$RMS\_PBUGCHK  
\$\$RMS\_TBUGCHK  
\$\$RMS\_UMODE  
DEV\$V\_FOR  
DEV\$V\_RND  
DEV\$V\_SQD  
IFBSB\_FAC  
IFBSL\_PRIM\_DEV  
IFBSM\_BIO  
IFBSV\_BIO  
IFBSV\_BRO  
IFBSV\_EOF  
IRBSV\_BIO\_LAST  
IRBSV\_EOF  
IRBSV\_PPF IMAGE  
PIOSA\_TRACE  
RMSDI\_COMMON  
RMSDISCONNECT1  
RMSWTLS1  
TPTSL\_DISCON1

= 00000000  
= 0000001A  
= 00000010  
= 00000008  
= 00000004  
= 00000018  
= 0000001C  
= 00000005  
= 00000022  
= 00000000  
= 00000020  
= 00000005  
= 00000006  
= 00000021  
= 00000027  
= 00000021  
= 00000022  
\*\*\*\*\* X 01  
\*\*\*\*\* X 01  
00000000 RG 01  
\*\*\*\*\* X 01  
\*\*\*\*\* X 01

+-----+  
! Psect synopsis !  
+-----+

PSECT name	Allocation	PSECT No.	Attributes																	
ABS	00000000	( 0.)	00 ( 0.)	NOPIC	USR	CON	ABS	LCL	NOSHR	NOEXE	NORD	NOWRT	NOVEC	BYTE						
RMSRMS1	00000047	( 71.)	01 ( 1.)	PIC	USR	CON	REL	GBL	NOSHR	EXE	RD	NOWRT	NOVEC	BYTE						
SABSS	00000000	( 0.)	02 ( 2.)	NOPIC	USR	CON	ABS	LCL	NOSHR	EXE	RD	WRT	NOVEC	BYTE						

+-----+  
! Performance indicators !  
+-----+

Phase	Page faults	CPU Time	Elapsed Time
Initialization	36	00:00:00.07	00:00:00.92
Command processing	137	00:00:00.68	00:00:04.70
Pass 1	201	00:00:04.38	00:00:15.95
Symbol table sort	0	00:00:00.54	00:00:01.01
Pass 2	43	00:00:00.82	00:00:02.89
Symbol table output	4	00:00:00.02	00:00:00.11
Psect synopsis output	1	00:00:00.02	00:00:00.08
Cross-reference output	0	00:00:00.00	00:00:00.00
Assembler run totals	424	00:00:06.56	00:00:25.67

The working set limit was 1200 pages.

22790 bytes (45 pages) of virtual memory were used to buffer the intermediate code.

There were 30 pages of symbol table space allocated to hold 444 non-local and 7 local symbols.

155 source lines were read in Pass 1, producing 13 object records in Pass 2.

15 pages of virtual memory were used to define 14 macros.

RM1DISCON  
VAX-11 Macro Run Statistics

DISCONNECT FOR SEQ. ORG.

N 10

16-SEP-1984 00:47:00 VAX/VMS Macro V04-00  
5-SEP-1984 16:23:17 [RMS.SRC]RM1DISCON.MAR;1

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+-----+  
! Macro library statistics !  
+-----+

Macro library name

-----  
\$255\$DUA28:[RMS.OBJ]RMS.MLB;1  
-\$255\$DUA28:[SYS.OBJ]LIB.MLB;1  
-\$255\$DUA28:[SYSLIB]STARLET.MLB;2  
TOTALS (all libraries)

Macros defined

-----  
6  
0  
4  
10

531 GETS were required to define 10 macros.

There were no errors, warnings or information messages.

MACRO/LIS=LIS\$:RM1DISCON/OBJ=OBJ\$:RM1DISCON MSRC\$:RM1DISCON/UPDATE=(ENH\$:RM1DISCON)+EXECML\$/LIB+LIB\$:RMS/LIB

0321 AH-BT13A-SE  
VAX/VMS V4.0

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RMICONN  
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RMIGETINT  
LIS

RMICREATE  
LIS

RMIJOURNAL  
LIS